

**ABSTRACT OF THE DISCLOSURE**

A host is provided that includes a processor, a host memory coupled to the processor, and a host-fabric adapter coupled to the processor and provided to interface with a switched fabric including one or more fabric-attached I/O controllers. The host-fabric adapter may include logic  
5 for accessing a translation and protection table from the host memory for a data transaction. The translation and protection table may include a plurality of translation entries and at least one region entry. The at least one region entry may include multiple levels of translation entries.

219.38575X00  
LID14352/P9059